



PTO/SB/088 (08-03)  
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/766,116
Filing Date	January 27, 2004
First Named Inventor	Guterman
Art Unit	2827
Examiner Name	V. Q. Nguyen
Attorney Docket Number	SAND-01016US0

Sheet 3 of 3

### NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
W		KURATA, Hideaki, et al., Constant-Charge-Injection Programming for 10-MB/s Multilevel AG-AND Flash Memories, 2002 Symposium On VLSI Circuits Digest of Technical Papers, pp. 302-303	✓
W		JOHNSON, William S., et al., SESSION XII: ROMs, PROMs and EROMs, 1980 IEEE International Solid State Circuits Conference, pp. 152-153	✓
W		NOBUKATA, Hiromi, et al., A 144Mb 8-Level NAND Flash Memory with Optimized Pulse Width Programming, 1999 Symposium on VLSI Circuits Digest of Technical Papers, pp. 39-40	✓
W		OHKAWA, Masayoshi, et al., TP 2.3: A 98 mm <sup>2</sup> 3.3V 64Mb Flash Memory with FN-NOR Type 4-level Cell, 1996 IEEE International Solid-State Circuits Conference, pp. 36-37	✓

Examiner Signature	V. Nguyen	Date Considered	9/2/05
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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## U. S. PATENT DOCUMENTS

[illegible]

**FOREIGN PATENT DOCUMENTS**

[illegible]

Examiner  
Signature

V. NGIzezen

Date	Considered
11/1/80	11/1/80
11/2/80	11/2/80
11/3/80	11/3/80
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